

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Joseph Maggolino

Date: November 20, 2000

Serial No.: 09/553,756

Group Art Unit: 2862

Filed: April 21, 2000

Examiner: Unassigned

For: CIRCUITRY FOR HIGH VOLTAGE LINEAR CURRENT SENSE IC

Asst. Commissioner for Patents
Washington, D.C. 20231

RECEIVED
NOV 20 2000
TECHNOLOGY CENTER 2800

PRELIMINARY AMENDMENT

Sir:

Prior to examination on the merits, please amend the above-identified application as follows:

IN THE CLAIMS:

Please cancel claim 4.

Please amend claim 1 as follows:

Sub B
5

1. (Amended) A current sense integrated circuit, comprising:
an amplifier circuit for receiving and amplifying a [high voltage] differential analog input signal at a high voltage level containing current sense information;
a pulse width modulator circuit for converting the [amplifier high voltage] differential analog input signal to a [high voltage] pulse width modulated signal at the high voltage level;
a level shift circuit for converting the [high voltage] pulse width modulated signal [to a lower reference potential] from the high voltage level to a low voltage level; and